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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10041792	FILING DATE 10/23/2001	CLASS 336	SUBCLASS 002.1	GAU 3729 2832	EXAMINER Kim
<b>**APPLICANTS:</b> Roy Apurba; Eiranova Florencio;					
<b>**CONTINUING DATA VERIFIED:</b> None per					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> None per					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no				ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no				13431-6	
Verified and Acknowledged Examiners's initials <i>pl</i>					
TITLE : Fully automatic process for magnetic circuit assembly					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner		CLAIMS ALLOWED	
				Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner		DRAWING	
Amount Due	Date Paid			Sheeto Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE		Application Examiner	
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